Pre-Workshop Tutorials

December 04, 2012 - PCaPAC-2012

9.00	-	10.00	Registration

Tutorial – I
Venue: Silver Jubilee Guest House Conference Room
Topic: EPICS & CSS Tutorial Seminar and Hands-on
Speaker: Norihiko Kamikubota- High Energy Accelerator Research Organization, Japan

		10.00	Tutorial
11.00	-	11.30	Tea Break
		11.30	Tutorial Continued
13.15	-	14.30	Lunch
		14.30	Tutorial Continued
15.30	-	16.00	Tea Break
		16.00	Tutorial Continued

Tutorial – II
Venue: Ajay Divatia Lecture Hall
Topic: Programming EPICS enabled Real-Time and FPGA
Speaker: Arun Veeramani - National Instruments

		10.00	Tutorial
11.00	-	11.30	Tea Break
		11.30	Tutorial Continued
13.15		14.30	Lunch
		14.30	Tutorial Continued
15.30		16.00	Tea Break
16.00		17.30	Tutorial Continued

Workshop Programme

December 05, 2012 - PCaPAC-2012

Venue: VECC/SINP Campus Auditorium Main Lecture Hall

8.30 - 9.30	Registration	

9.30 - 11.00	**Inaugural Session**

9.30	**Welcome Address**, Ranadhir Dey, Head, CPIES Group, VECC
9.40	**About the Workshop**, Debranjan Sarkar, Chairman, PCaPAC 2012
9.50	**Keynote Address,** Matthias R. Clausen – DESY, Germany **WEKA01** The CSS Story
10.20	**Keynote Address,** Y S Mayya - BARC , India **WEKA02** Evolution of Control Systems for Large Telescopes and Accelerators : A retrospective
10.50	**Vote of Thanks**, Sarbajit Pal, Secretary, PCaPAC 2012

11.00 - 12.00	Inauguration of Exhibition & High-Tea

12.00 - 13.15	WEIB **Overview of Control System** Chair: Chandra Kant Pithawa – BARC, India

12.00	**WEIB01** Implementation of Control Systems for Cyclotrons at VECC: A Status Report Speaker: Sarbajit Pal – VECC, India
12.25	**WEIB02** Review of Control Resources for J-PARC Accelerators Speaker: Norihiko Kamikubota – KEK, Japan
12.50	**WEIB03** Indus-2 Control System: A Closer Perspective Speaker: Pravin Fatnani – RRCAT, India

13.15	13.30	Group Photo
13.30 - 14.45		Lunch

14.45 - 15.40	WEIC and WECC **Latest Trends in GUI** Chair: Y S Mayya – BARC, India

14.45	**WEIC01** Web2cToGo: Bringing the Web2cToolkit to Mobile Devices Speaker: Reinhard Bacher – DESY, Germany

15.10	**WECC02** EPICS Channel Access Using WebSocket
	Speaker: Akito Uchiyama - Sokendai, Japan
15.25	**WECC03** Qt Based GUI System for EPICS Control Systems
	Speaker: Ricardo Nogueira Fernandes – Australian Synchrotron Company, Australia

15.40 - 17.00	Poster & Tea
17.00 - 18.30	VECC Tour
18.30 - 22.30	Banquet

December 06, 2012 - PCaPAC-2012

Venue: VECC/SINP Campus Auditorium Main Lecture Hall

| 9.30 - 11.50 | THIA and THCA **Status Report of Control System** |
| | Chair: Reinhard Bacher – DESY, Germany |

9.30	**THIA01** Trombay Programmable Logic Controller TPLC-32
	Speaker: Uday W. Vaidya – BARC, India
9.55	**THIA02** Current Status and Upgrade Plan of the Data-Acquisition System in SACLA
	Speaker: Takashi Sugimoto – JASRI, Japan
10.20	**THIA03** The IUAC Tandem-LINAC Control System
	Speaker: Ajith Kumar – IUAC, India
10.45	**THCA04** An Update on ConSys Including a New LabVIEW FPGA Based LLRF System
	Speaker: Torben Worm – ISA, Denmark

| 10.45 - 11.20 | Tea Break |

11.20	**THCA05** PLC-based Control System for 10 MeV Linear Accelerator at EBC Kharghar, BARC
	Speaker: Alka S Chachondia -BARC, India
11.35	**THCA06** Status of the Ultra Fast Tomography Experiments Control at ANKA
	Speaker: David Haas – KIT, Germany

| 11.50 - 13.00 | THCB **Control Database and Control system Interoperability** |
| | Chair: Takashi Kosuge -KEK, Japan |

11.50	**THCB01** HyperArchiver: an Evolution of EPICS Channel Archiver
	Speaker: María del Campo - ESS Bilbao Consortium, Spain
12.05	**THCB02** EPICS MySQLArchiver - Integration Between EPICS and MySQL

Speaker: Anindya Roy – VECC, India

12.20 **THCB03** Using Memcached as Real-time Database in the SPARC Control System

Speaker: Giampiero Di Pirro – INFN, Italy

12.35 **THIB04** Control System Interoperability, an Extreme Case: Merging DOOCS and TINE

Speaker: Philip Duval – DESY, Germany

12.35 - 13.15	Group Photo
13.15 - 14.30	Lunch
14.30 - 15.40	THIC and THCC **Software and Hardware Technology**
	Chair: Debranjan Sarkar -VECC, India

14.30 **THIC01** Tango for Experiment Control

Speaker: Jens Meyer – ESRF, France

14.55 **THCC02** Controls Architecture for the Diagnostic Devices at the European XFEL

Speaker: Olaf Hensler – DESY, Germany

15.10 **THCC03** PC Based Real Time Data Exchange on 10GbE Optical Network Using RTOS

Speaker: Rajeev Prasad Gupta – IPR, India

15.10 - 15.45	Tea Break
15.45 - 16.30	THCD **Software and Hardware Technology & Data Integrity and Security**
	Chair: Ralph C. Baer – GSI, Germany

15.45 **THCD04** Master Slave Topology Based, Remotely Operated, Precision X-ray Beam Profiler and Placement System for High Pressure Physics Experiment at Indus-2 Beam Line

Speaker: Harilal Savadas Vora – RRCAT, India

16.00 **THCD05** A Flexible and Testable Software Architecture: Applying Presenter First to a Device Server for the DOOCS Accelerator Control System of the European XFEL

Speaker: Andreas Beckmann - European XFEL, Germany

16.15 **THCD06** Design Development and Analysis of a Comprehensive Open Source System for Proactive Management of Security Aspects of a Control Network

Speaker: Shailendra Singh Tomar -RRCAT, India

16.30 - 18.00	Poster
18.30 - 19.30	Cultural Programme
19.30 - 22.30	Dinner

December 07, 2012 - PCaPAC-2012

Venue: VECC/SINP Campus Auditorium Main Lecture Hall

9.30	-	10.40	FRIA and FRCA **Status Report of Control System**

Chair: Philip Duval – DESY, Germany

	9.30	**FRIA01** The New White Rabbit Based Timing System for the FAIR Facility

Speaker: Dietrich Hans Beck – GSI, Germany

	9.55	**FRCA02** Status Report and Maintenance Issues of VME Based Cryogenic Control System at IUAC

Speaker: Joby Antony – IUAC, India

	10.10	**FRCA03** Development of a Car-borne Survey System KURAMA

Speaker: Minoru Tanigaki – KUR, Japan

	10.25	**FRCA04** Control System for BARC-TIFR Pelletron

Speaker: Sudheer Singh – BARC, India

10.40	-	11.05	Tea Break

11.05	-	12.05	FRCB **Status Report of Control System**

Chair: Pravin Fatnani – RRCAT, India

	11.05	**FRCB01** Maintaining an Effective and Efficient Control System for the Electromagnetic Calorimeter of the Compact Muon Solenoid Experiment During Long-term CERN Large Hadron Collider Operations

Speaker: Oliver Holme - ETH Zurich IPP, Switzerland

	11.20	**FRCB02** Development of the Control System for PEFP 100-MeV Proton Linear Accelerator

Speaker: Young-Gi Song – KAERI, Korea

	11.35	**FRCB03** RF Control System for 400 keV RFQ

Speaker: Sandeep Kashinath Bharade – BARC, India

	11.50	**FRCB04** VEPP-2000 Collider Control System

Speaker: Alexander Senchenko – BINP, Russia

12.05	-	13.05	FRCC **PC vs Embedded Systems & Experimental Data Acquisition**

Chair: Amitava Roy – VECC, India

	12.05	**FRCC01** Design of the Data Acquisition System for the Nuclear Physics Experiments at VECC

Speaker: Partha Dhara – VECC, India

	12.20	**FRCC02** A FPGA Based High Speed Data Acquisition Card

Speaker: Jaydeep Ashok Gore – BARC, India

12.35 **FRCC03** Development and Performance Analysis of EPICS Channel Access Server on FPGA based Soft-core Processor

Speaker: Shantonu Sahoo – VECC, India

12.50 **FRCC04** Digital Pulse Processing Techniques for High Resolution Amplitude Measurement of Radiation Detector

Speaker: Payal Singhai – VECC, India

| 13.05 | - | 14.30 | Lunch |
| 14.30 | - | 14.45 | Group Photo |

Venue: Ajay Divatia Lecture Hall

| 14.45 | - | 15.25 | FRID and FRCD **Verification & Validation** Chair: Wolfgang Mexner – KIT, Germany |

14.45 **FRID01** Introducing the !CHAOS Control Systems Framework

Speaker: Luciano Catani – INFN, Italy

15.10 **FRCD02** Process Control for Parallel Run of Two Helium Liquefiers at VEC Centre, Kolkata

Speaker: Sandip Pal – VECC, Kolkata

| 15.25 | - | 15.45 | Tea Break |
| 15.45 | | 16.45 | Valedictory Session |